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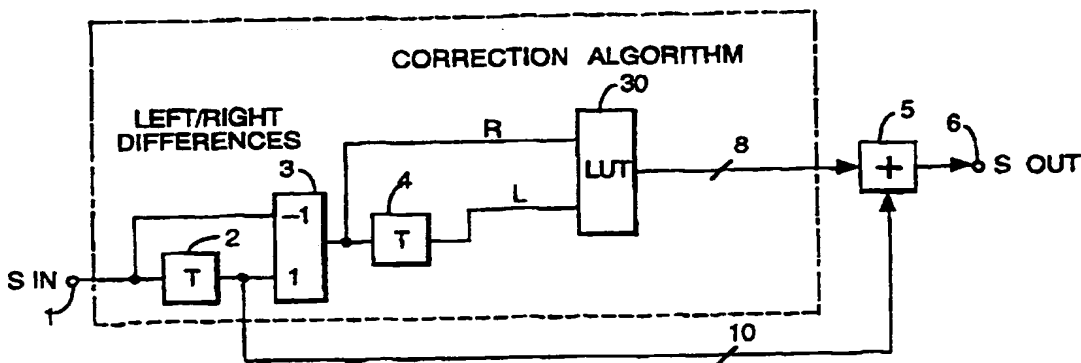
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(54) Title: VIDEO SIGNAL PROCESSING

SINGLE WINDOW ENHANCER



(57) Abstract

A video signal is enhanced using a non-linear combination of left and right difference signals (L and R), constrained to be zero if either L or R is zero, using a minimum absolute value function. Edge enhancement is provided when the polarities of L and R are the same and peak enhancement when the polarities are different. Difference signals are derived with delays (2 and 4) and a subtractor (3). The non-linear combinations are derived in a lookup table (30).

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- 1 -

VIDEO SIGNAL PROCESSING

This invention relates to the processing of analogue and digital video signals and is particularly concerned with restoration of picture sharpness, ie. with recovery of wanted high frequency signal components lost or reduced in the process of video recording or transmission. Traditionally, such processing is known as aperture correction or edge enhancement.

A well known enhancement technique is to determine the second differential of the signal, multiply it by a coefficient (which determines the degree of correction) and add the result to the input signal. The frequency response of such an enhancer is linked to its pulse response by the well-known Fourier transform. A fundamental limitation of this kind of enhancer is that it is impossible to boost the high frequency components without visible and annoying overshoots in the pulse and step responses.

There is another known, non-linear edge enhancement technique based on the synthesis of high frequency components by extension of the low-frequency part of the signal spectrum. This approach includes the calculation of the first and second derivatives of an input signal, then multiplication of said first derivative by the clipped second derivative and, finally, summation of the resulting correction signal with the incoming signal. Such processing provides improved pulse response, although the pulse response is not completely free from unwanted overshoots or ripples, and periodic picture textures are degraded.

The object of this invention is to provide an edge enhancer which offers improvement in the sharpness of picture edges. A further object is to provide a peak enhancer, which offers improvement in the contrast of lines and high-frequency textures without visible artefacts. It is a further object of this invention to provide an improved enhancer capable of combining both signal steepness correction and "peaking" on sinusoidal textures and the tops of short pulses.

Accordingly, the present invention consists in a method of video signal processing comprising the steps of forming left and right difference signals

and deriving an enhancement signal through non-linear combination of said difference signals, wherein the enhancement signal is constrained to be zero if either the left or the right difference signal is zero.

The separate use of left and right difference signals gives superior and more flexible correction than prior-art enhancers.

Advantageously, the enhancement signal comprises an edge enhancement signal when the polarities of the left and right difference signals are the same and a peak enhancement signal when the polarities of the left and right difference signals are different.

It is a still further object of this invention to provide an improved enhancer in the form of a circuit which calculates the best of the output correction signals from several constituent enhancers differing in the size of their "correction windows", ie. the delay between the signal samples involved in the left/right difference calculations.

Preferably, the method further comprises the step of deriving a plurality of enhancement signals with respect to different time intervals T_i to T_N and selecting one enhancement signal for use.

Both analogue and digital implementations are possible. Particularly in the case of analogue implementation, the processing can be done in many ways with equivalent results; the variants shown below must be regarded only as examples.

This invention will now be described by way of example with reference to the accompanying drawings in which:

Figure 1 is a general block diagram of an edge and peak enhancer according to the present invention;

Figure 2 is a detailed view of a component used a number of times in the enhancer shown in Figure 1;

Figure 3 is a family of plots illustrating step and pulse responses at important points within the enhancer shown in Figure 1.

Figure 4 is a diagram showing one variant of a digital implementation of the components shown in block form in Figure 1.

Figure 5 is a diagram showing the "multiple windows" variant of the

- 3 -

enhancer shown in block form in Figures 1 and 3.

Referring initially to Figure 1, a signal S_{in} at an input terminal 1 passes through a delay line 2 and an adder 5 to an output terminal 6. The output of the delay line 2 is also taken to the non-inverting input of a subtracter 3 which receives the signal S_{in} at its inverting input. In this way, the current slope of this signal is evaluated by the subtracter 3. The right difference signal R from the output of the subtracter 3 is fed to the input of a second delay line 4, providing at its output the left difference signal L . The right difference signal R is also taken to an inverter 11 providing at its output the inverted right difference signal $-R$. The right and left difference signals are compared with zero in respective comparators 12 and 13, for a purpose to be described. They are also fed to the inputs of an adder 14, providing a second derivative signal $(L-R)$, and fed to the inputs of a minimum magnitude detector 15.

The minimum magnitude detector provides the function $\text{Minm}(A,B)$ where:

$$\text{Minm}(A,B) = \text{if } \text{abs}(A) < \text{abs}(B) \text{ then } A \text{ else } B$$

Figure 2 shows one example of a possible implementation of a minimum magnitude detector. It works as follows: a comparator, which is fed by the absolute values of the input signals, controls a switch selection in such a way that the signal with smaller magnitude is selected for further processing.

Returning to Figure 1, the signals from the outputs of the adder 14 and minimum magnitude detector 15 are fed, respectively, through gain control amplifier 17 and inverter 18 to a further minimum magnitude detector 21 to provide an edge correction signal at the terminal 24 of switch 23.

This edge correction signal C_{edge} takes the form:

$$C_{edge} = \text{Minm}(G_e * (L-R), -\text{Minm}(L,-R))$$

- 4 -

In a similar way the outputs of the adder 14 and minimum magnitude detector 15 are fed, respectively, through gain control amplifiers 19 and 20 to minimum magnitude detector 22 to provide a peaking enhancement signal at the terminal 25 of switch 23. This signal C_{peak} takes the form:

$$C_{peak} = \text{Minm} (0.5 * G_p (L-R), G_p * \text{Minm} (L, -R))$$

The comparison of the polarities of the L and R signals in the exclusive-or gate 16 provides the control signal for the switch 23 which determines whether "edge" or "peak" correction should be applied. If the polarities are the same, edge correction is applied, if the polarities are different, peak correction.

The degree of "edge" enhancement is set by the gain of the amplifier 17 (indicated as the parameter G_e in the figure).

The gain of amplifier 19 is required to be half that of amplifier 20. The degree of "peak" enhancement is set by these gains (indicated by the parameter G_p in the figure).

The complete edge and peak correction signal at the output terminal of switch 26 is added to the input signal in the adder 5 to form the enhanced output signal S_{out} at the terminal 6.

The correction signal is always a result of a minimum difference selection and so the level of an enhanced transient never exceeds the adjacent flat-area level, which means that overshoots can never occur. Put another way, the enhancement or correction signal is constrained to be zero if either the left or the right difference signal is zero.

Although Figure 1 shows a switch to select between the edge and peak correction signals no discontinuities in the correction signal are introduced when the switch changes state. This is because the comparators feeding the exclusive-or gate only change state at zero crossing of L or R. When L is at zero both inputs to the switch are equal (at a voltage of -R) and when R is at zero both switch inputs are zero.

Figure 3 shows the shapes of the waveforms at some important

- 5 -

points in Figure 1.

Figure 3a shows an input signal consisting of a wide pulse and sine-squared pulse with a rise-time approximately equal to the delay T in the delay lines 2 and 4 on Figure 1.

5 In Figure 3b the left and right difference (derivative) signals are shown.

In Figure 3c the edge correction signal is shown. Note that when both difference signals are equal the polarity of the correction signal changes. Immediately before and after the polarity change (when the left and right differences are approximately equal) the signal is controlled by the change of slope of the input signal ie. the signal selected by minimum magnitude selector is the second derivative multiplied by the control parameter G_e .

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In Figure 3d the peaking correction signal is shown. This signal exists only where the polarities of left and right differences are different and reaches its maximum at the top of the pulse.

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In Figure 3e the output enhanced signal is shown. Note that when one of the difference signals changes polarity the correction changes between the edge and peak enhancement modes. The important advantage of the enhancer according to the present invention is that the correction signal is cancelled at flat areas, in particular at the baseline of the waveform before and after pulses. Hence there are no unwanted ripples or overshoots.

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Figure 4 shows a digital implementation of the enhancer. The input signal passes through the blocks 2, 3, 4 and 5, identical to these shown in Figure 1, to the output 6. The two differences signals, L and R, are fed to the inputs of the look-up table 30. This combines the functions of all the blocks within the dotted line rectangle shown in Figure 1. Because the output of the look-up table is a correction signal, rather than the main path signal, it can be quantised with less levels than the main path, for instance, with 8 bits instead of 10. Obviously, the use of a look-up table for all the non-linear operations makes the digital implementation quite economical

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- 6 -

and compact.

Figure 5 shows an improved enhancer in which the input signal S_n is fed to several primary enhancers 40, 41..50, differing only in the size of correction window T, ie. the delay between signal samples involved in the left/right differences calculation. The outputs of these primary enhancers are combined in a maximum magnitude selector 60 so that the highest level correction signal is always selected, and the enhancer automatically adapts itself to the bandwidth (sharpness) of incoming signal. The implementation of the maximum magnitude selector is similar to the minimum magnitude selector shown in Figure 2.

With digital hardware it is possible to do more than one set of calculations in one sampling interval and so the parallel processing shown may in fact be provided merely by an increase of the system clock rate above the sampling rate and using the part of the hardware several times over.

CLAIMS

1. A method of video signal processing comprising the steps of forming left and right difference signals L and R, and deriving an enhancement signal through non-linear combination of said difference signals, wherein the enhancement signal is constrained to be zero if either L or R is zero.
2. A method according to Claim 1, wherein the enhancement signal is constrained to be zero if either the left or the right difference signal is zero, through operation of a function which selects of its arguments that having the minimum absolute value.
3. A method according to Claim 1 or Claim 2, wherein the enhancement signal comprises an edge enhancement signal when the polarities of the left and right difference signals are the same and a peak enhancement signal when the polarities of the left and right difference signals are different.
4. A method according to any one of the preceding claims, wherein the enhancement signal comprises an edge enhancement signal C_{edge} , where

$$C_{edge} = \text{Minm} (G_e * (L-R), - \text{Minm} (L, -R))$$

and where the function Minm is defined by

$$\text{Minm}(A,B) = \text{if } \text{abs}(A) < \text{abs}(B) \text{ then } A \text{ else } B.$$

- 8 -

5. A method according to any one of the preceding claims, wherein the enhancement signal comprises an edge enhancement signal C_{peak} , where

$$C_{peak} = \text{Minm} (0.5 * G_p (L-R), G_p * \text{Minm} (L,-R))$$

and where the function Minm is defined by

5 **Minm(A,B) = if abs(A) < abs (B) then A else B.**

6. A method according to Claim 1, wherein one difference signal is calculated by subtracting a signal delayed by time interval T and the other difference signal is derived through delaying said one difference signal by time interval T.

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7. A method according to Claim 6, comprising the step of deriving a plurality of enhancement signals with respect to different time intervals T_1 to T_N and selecting one enhancement signal for use.

8. A method according to Claim 7, wherein the enhancement signal having the largest magnitude is selected for use.

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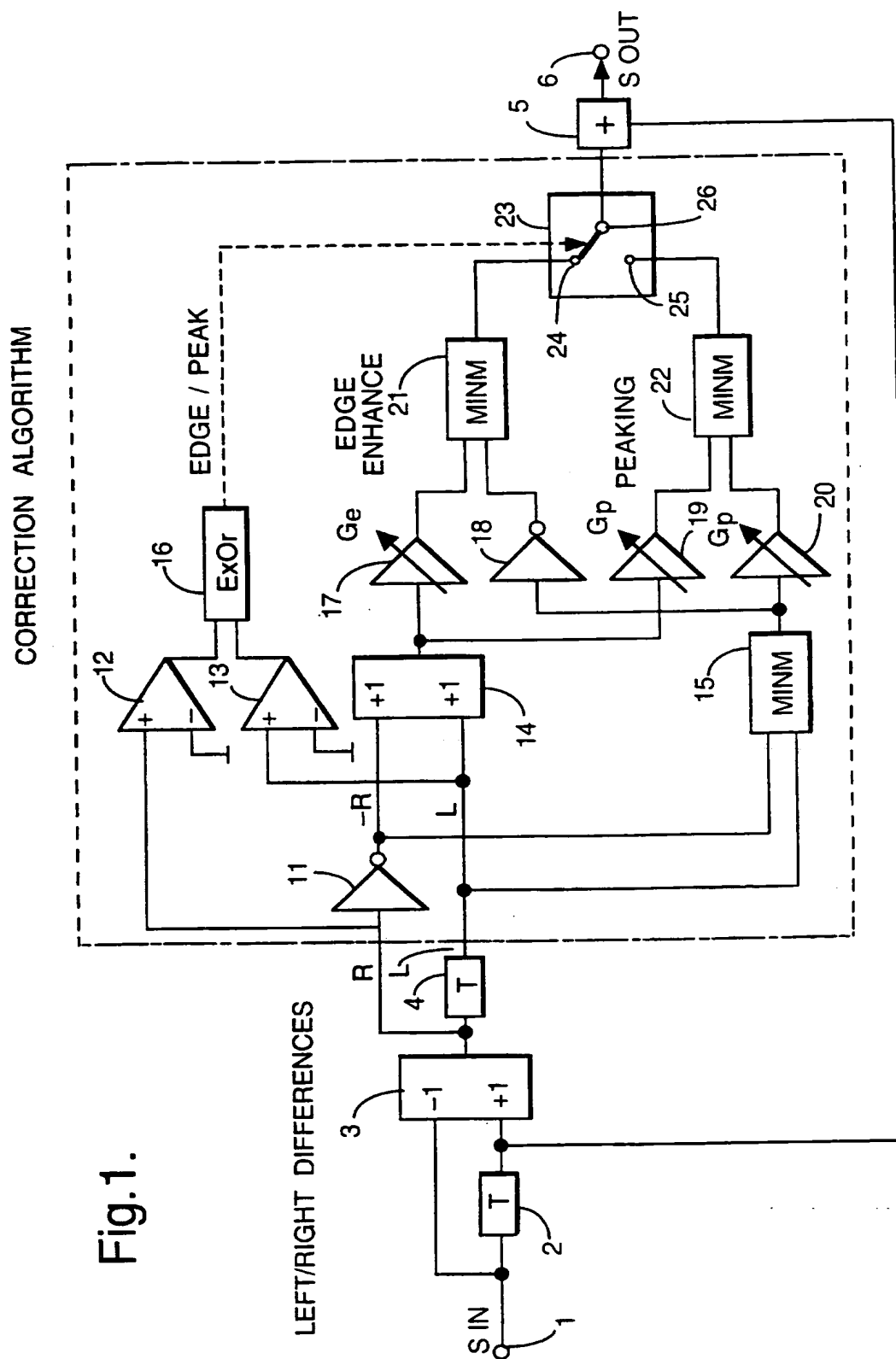


Fig.2.

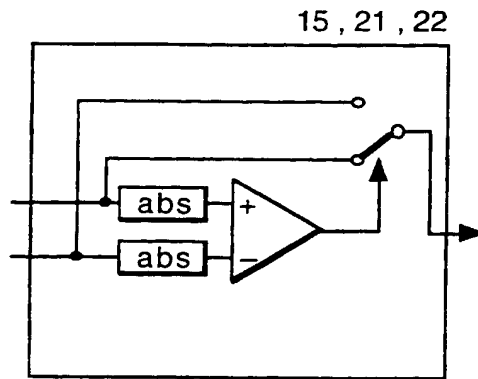
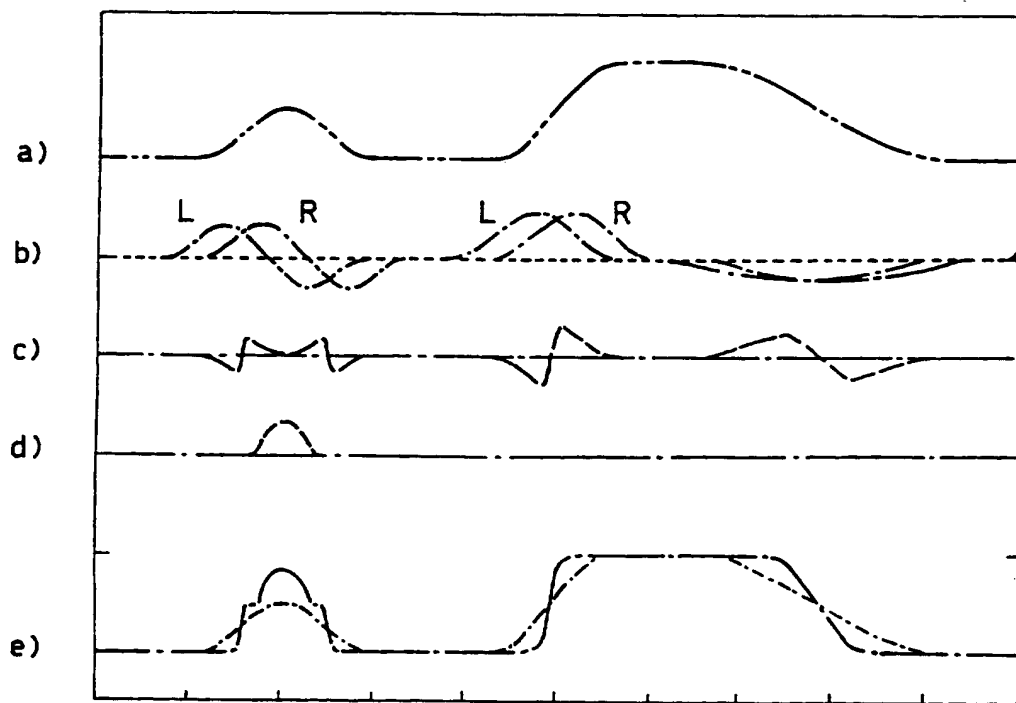


Fig.3.



- a) INPUT SIGNAL
- b) LEFT AND RIGHT DIFFERENCES
- c) EDGE CORRECTION SIGNAL
- d) PEAKING CORRECTION SIGNAL
- e) ENHANCED SIGNAL

Fig.4.

SINGLE WINDOW ENHANCER

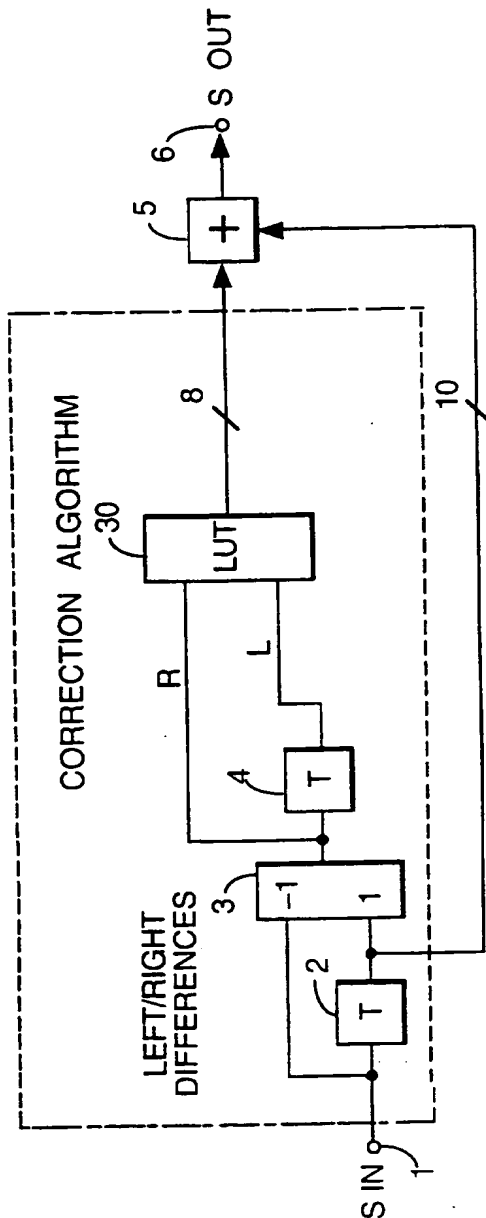
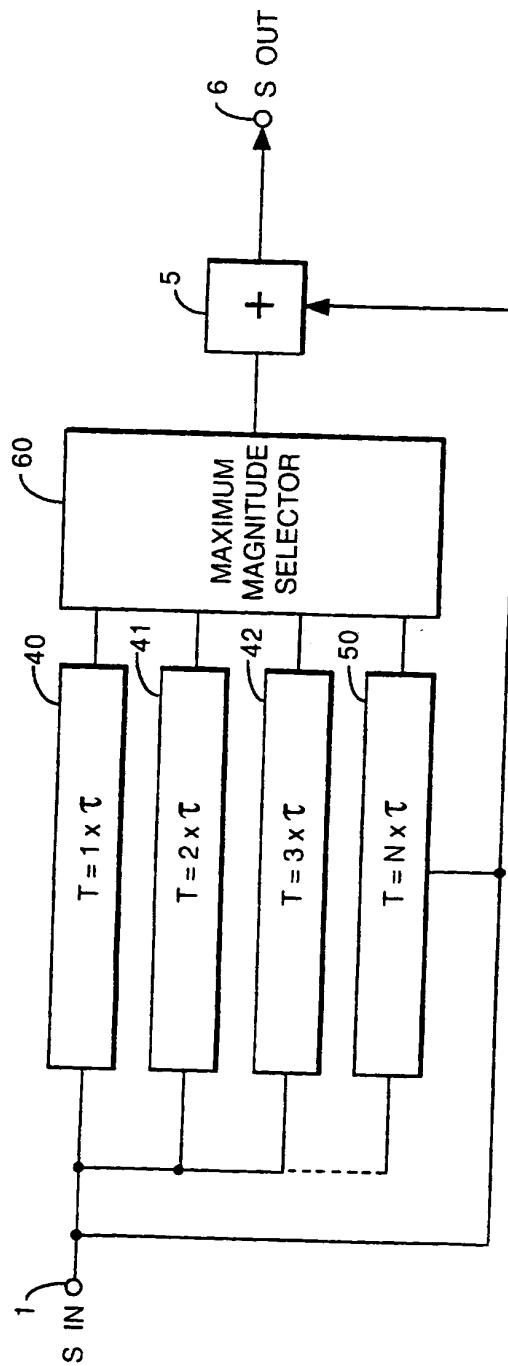


Fig.5.

N SINGLE WINDOW ENHANCERS



INTERNATIONAL SEARCH REPORT

International Application No
PCT/GB 96/02402

A. CLASSIFICATION OF SUBJECT MATTER
IPC 6 H04N5/208

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	PATENT ABSTRACTS OF JAPAN vol. 14, no. 524 (E-1003), 16 November 1990 & JP,A,02 222267 (TOSHIBA), 5 September 1990, see abstract; figure 2	1,2,6
X	EP,A,0 505 999 (MATSUHITA) 30 September 1992 see abstract; figures 1,2	1,2,6
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Information on patent family members

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP-A-505999	30-09-92	JP-A- 4296171	20-10-92
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